

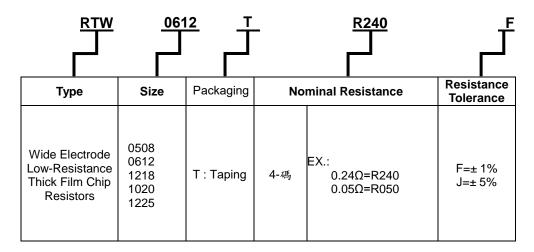
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1 Scope:

- 1.1 This specification is applicable to lead free and halogen free of RoHS directive for RTW series wide terminal Low-Resistance thick film chip resistors.
- 1.2 The product is for general electronic purpose.

2 Explanation Of Part Numbers:

(EX)



3 General Specifications:

	Rated	Max.	Max.	T.C.R	Resistance Range
Туре	Power at 70°C	Rated Current	Overload Current	(ppm / °C)	F(±1%) \cdot J((±5%) E-24 \cdot E-96
				±800	$10m\Omega \le R < 30m\Omega$
RTW0508	1 ,,,	5.77A	14.424	±400	$30m\Omega \le R < 56m\Omega$
	$\frac{1}{3}$ W	5.77A	14.43A	±200	$56m\Omega \leq R < 180m\Omega$
				±200	$180 \text{m}\Omega \leq R < 1\Omega$
				±2000	$10m\Omega {\le} R {<} 30m\Omega$
RTW0612	1W	104	25A	±1000	30 m Ω \leq R $<$ 56 m Ω
	IVV	10A	25A	±700	$56m\Omega \leq R < 180m\Omega$
				±250	$180m\Omega \le R < 1\Omega$
				±2000	$10m\Omega \le R < 30m\Omega$
RTW1218	1W	10A	25A	±1000	30 m Ω \leq R $<$ 56 m Ω
	177	IUA	25A	±700	$56m\Omega \leq R < 180m\Omega$
				±250	$180m\Omega \leq R < 1\Omega$
				±800	$10m\Omega \leq R < 30m\Omega$
RTW1020	1W	10A	25A	±400	$30m\Omega \leq R < 56m\Omega$
	IVV	IUA	25A	±200	$56m\Omega \leq R < 180m\Omega$
				±200	$180 \text{m}\Omega \! \leq \! R \! < \! 1\Omega$
				±800	$10m\Omega \leq R < 30m\Omega$
RTW1225	2W	14.14A	35.35A	±400	$30m\Omega \le R < 56m\Omega$
	∠vv	14.144	33.33A	±200	$56m\Omega \le R < 180m\Omega$
				±200	$180 \text{m}\Omega \leq R < 1\Omega$
	Operating Temperature Range				-55℃ ~+155℃

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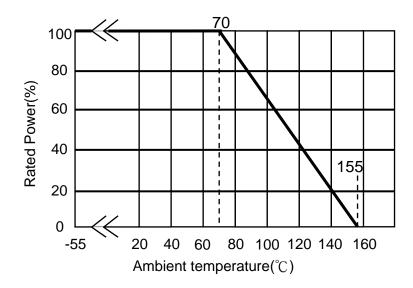


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3.1 Power Derating Curve:

Operating Temperature Range : - 55~155 ℃

If the ambient temperature exceeds 70 degrees centigrade to 155 degrees centigrade, the power can be modified by the curve as below.



3.2 Current Rating

Rated Current: DC current or AC current (rms) based on the rated power.

The current can be calculated by the following formula. If the calculated value exceeds the Max. current specified in the Table 3.2, the Max. current rating is set as the current rating.

$$I = \sqrt{P/R}$$
 |= Rate Per Po

I= Rated current (A)

P= Power rating (w)

R= Nominal resistance(Ω)

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4 Dimensions:

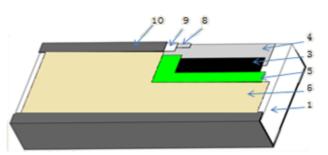
Unit:mm



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Туре	Dimension Size Code	L	W	Н	L1	L2
RTW	0508	1.20±0.10	2.00±0.10	0.50±0.10	0.20±0.10	0.20±0.15
RTW	0612	1.60±0.20	3.20±0.20	0.55±0.10	0.35±0.15	0.25±0.15
RTW	1218	3.10±0.10	4.60±0.20	0.55±0.10	0.45±0.25	0.40±0.20
RTW	1020	2.50±0.20	5.00±0.20	0.55±0.10	0.25±0.20	0.90±0.20
RTW	1225	3.20±0.20	6.40±0.20	0.55±0.10	0.45±0.20	0.75±0.20

5 Structure Graph:

Bottom side



Top Side



1	Ceramic substrate	6	2nd Protective coating
2	1st Top inner electrode	ner electrode 7 G2 layer+Marking	
3	Resistive layer	8	Terminal inner electrode
4	Bottom inner electrode	9	Ni plating
5	1st Protective coating	10	Sn plating

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6 ReliabilityTest:

6.1 Electrical Performance Test

Item	Conditions	Specifications
пеш	Conditions	Resistors
Coefficient of	TCR (ppm / $^{\circ}$ C) = $\frac{(R^2-R^1)}{R^1 (T^2-T^1)} \times 10^6$ R1: Resistance at room temperature R2: Resistance at -55 $^{\circ}$ C or +125 $^{\circ}$ C T1: Room temperature T2: Temperature -55 $^{\circ}$ C or +125 $^{\circ}$ C	Refer to item 3. general specifications
Short Time Overload	Refer to JIS-C5201-1 4.8 RTW05/18/20 applied 2.5 times rated current for 5 seconds, RTW06 applied 2.0 times rated current for 5 seconds,RTW25 applied 2.5 times rated current for 2 seconds, Release the load for about 30 minutes, then measure its resistance variance rate. (Rated current refer to item 3. general specifications) Refer to JIS-C5201-1 4.13	1% \ 5%:∆R=±2.0%
Dielectric Withstand Voltage		No short or burned on the appearance.
Intermittent Overload	Put the tested resistor in chamber under temperature 25±2°C and load 2.5 times rated DC current for 1 sec on, 25 sec off, 10000+400/-0 test cycles, then it be left at no-load for 1 hour, then measure its resistance variance rate. Refer to JIS-C5201-1 4.13	ΔR=±5.0%

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ltom Conditions		Specifications	
Item	Conditions	Resistors	
Solderability	Preconditioning Put the tested resistor in the apparatus of PCT, at a temperature of 105°C, humidity of 100% RH, and pressure of 1.22×10 ⁵ Pa for a duration of 4 hours. Then after left the tested resistor in room temperature for 2 hours or more. Test method: The resistor be immersed into solder pot in temperature 235±5°C for 2 sec, then the resistor is left as placed under microscope to observed its solder area.	Solder coverage over 95%	
	Refer to JIS-C5201-1 4.17		
Resistance to Soldering	○Test method 1 (Solder pot test): The tested resistor be immersed into molten solder of 260+5/-0°C for 10 +1/-0 seconds. Then the resistor is left in the room for 1 hour. ○Test method 2 (Solder pot test): The tested resistor be immersed into molten solder of 260+5/-0°C for 30 +1/-0 seconds. Then the resistor is left as placed under microscope to observe its solder area.	Test item 1: (1).Variance rate on resistance	
	Preheating temperature: 350±10°C Electric iron preheating time: 3+1/-0 sec Preheating the electric iron on electrode termination, as after that step placed the iron over 60 min. and measured its resistance variance rate. Refer to JIS-C5201-1 4.18		
		ΔR%=±2.0%	
	OHM Meter Refer to JIS-C5201-1 4.33		

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6.3 Environmental Test

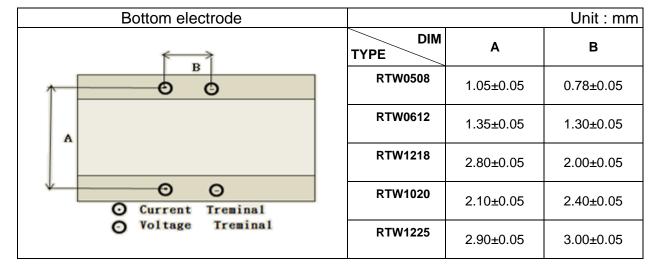
Item	Conditions	Specifications
		Resistors
	Put tested resistor in chamber under temperature 155±5°C for 1000 +48/-0 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate.(RTW0508、RTW0612for 125±3°C)	1% · 5% : △R=±2.0%
	Refer to JIS-C5201-1 4.25	
Thermal Shock	Put the tested resistor in the chamber under the Thermal Shock which shown in the following table shall be repeated 300 times consecutively. Then leaving the tested resistor in the room temperature for 1 hours, and measure its resistance variance rate.	1% 、 5% : ΔR=±2.0%
	Testing Condition	
	Lowest Temperature -55±5°C	
	Highest Temperature 125±5℃	
	Temperature-retaining time 15 minutes each	
	Refer to MIL-STD 202 Method 107	
•	Put the tested resistor in the chamber under temperature 40±2°C, relative humidity 90~95% and load the rated current for 90 minutes on, 30 minutes off, total 1000 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate.	1% · 5% : △R=±3.0%
	Refer to JIS-C5201-1 4.24	
Load Life	Put the tested resistor in chamber under temperature 70±2°C and load the rated current for 90 minutes on, 30 minutes off, total 1000 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate.	
	Refer to JIS-C5201-1 4.25	

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7 Measurement Point:



8 Plating Thickness:

8.1 Ni:**≧2** *μ* m

8.2 Sn(Tin): \ge 3 μ m

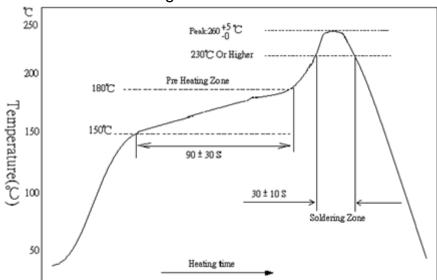
8.3 Sn(Tin):Matte Sn

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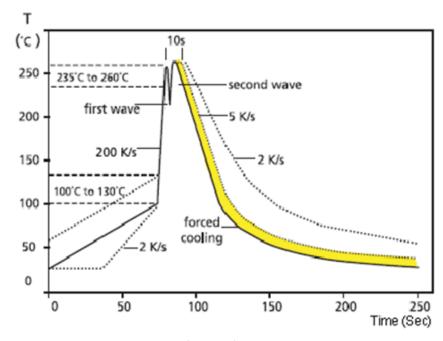
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- 9 Technical application notes: (This is for recommendation, please customer perform adjustment according to actual application)
 - 9.1 Recommend Soldering Method:
 - 9.1.1 Lead Free IR Reflow Soldering Profile



Remark: The peak temperature of soldering heat is 260 +5/-0° for 10 seconds

9.1.2 Lead Free Double-Wave Soldering Profile



9.1.3 Soldering Iron: temperature 350 $^{\circ}\text{C} \pm 10 ^{\circ}\text{C}$, dwell time shall be less than 3 sec.

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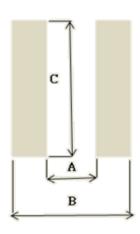


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Unit:mm

9.2 Land Pattern Design (For Reflow Soldering):

When a component is soldered, the resistance after soldering changes slightly depending on the size of the soldering area and the amount of soldering. When designing a circuit, it is necessary to consider the effect of a decrease or increase in its resistance.



			OTHL.IIIII
TYPE DIM	Α	В	С
RTW0508	0.6	2.2	2.3
RTW0612	0.7	2.6	3.5
RTW1218	1.9	4.1	4.9
RTW1020	0.5	3.5	5.3
RTW1225	1.3	4.2	6.4

9.3 Environment Precautions:

This specification product is for general electronic use, ABCO will not be responsible for any damage, cost or loss caused by using this specification product in any special environment. If other applications need to confirm with ABCO.

If consumer intends to use our Company product in special environment or condition (including but not limited to those mentioned below), then will need to make individual recognition of product features and reliability accordingly.

- (a) Used in high temperature and humidity environment
- (b) Exposed to sea breeze or other corrosive gas, such as Cl2 \ H2S \ NH3 \ SO2 and NO2.
- (c) Used in non-verified liquids including water, oil, chemical and organic solvents.
- (d) Using non-verified resin or other coating material to seal or coat our Company product.
- (e) After soldering, it is necessary to use water-soluble detergents to clean residual solder fluxes, even though no-clean fluxes are recommended.

9.4 Momentary Overload Precautions:

The product might be out of function when momentary overloaded. Please make sure to avoid momentary overloading while using and preserving.

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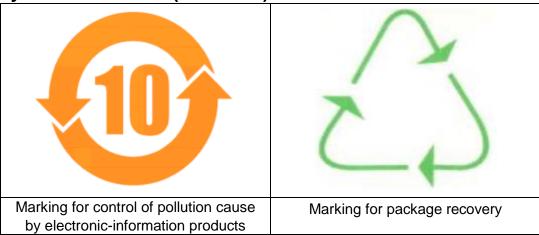
9.5 Operation and Processing Precautions:

- (a) Avoid damage to the edge of resistor and protective layer caused by mechanical stress.
- (b) Handle with care when printing circuit board (PCB) is divided or fixed on support body, because bending of printing circuit board (PCB) mounting will make mechanical stress for resistors.
- (c) Make sure the power rating is under the limit when using the resistor. When power rating is over the limit, the resister will be overloaded. There might be machinery damage due to the climbing temperature.
- (d) If the resister will be exposed under massive impact load (shock wave) in a short period of time, the working environment must be set up well before use.
- (e) Please make evaluation and confirmation when the product is well used in your company and have a through consideration of it's fail-safe design to ensure the system safety.

10 Storage and transportation requirement:

- 10.1 The temperature condition must be controlled at 25±5°C, the R.H. must be controlled at 60±15%. The stock can maintain quality level in two years.
- 10.2 Please avoid the mentioned harsh environment below when storing to ensure product performance and its' weldability. Places exposed to sea breeze or other corrosive gas, such as CI2 \ H2S \ NH3 \ SO2 and NO2.
- 10.3 When the product is moved and stored, please ensure the correct orientation of the box. Do not drop or squeeze the box. Otherwise, the electrode or the body of the product may be damaged.

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